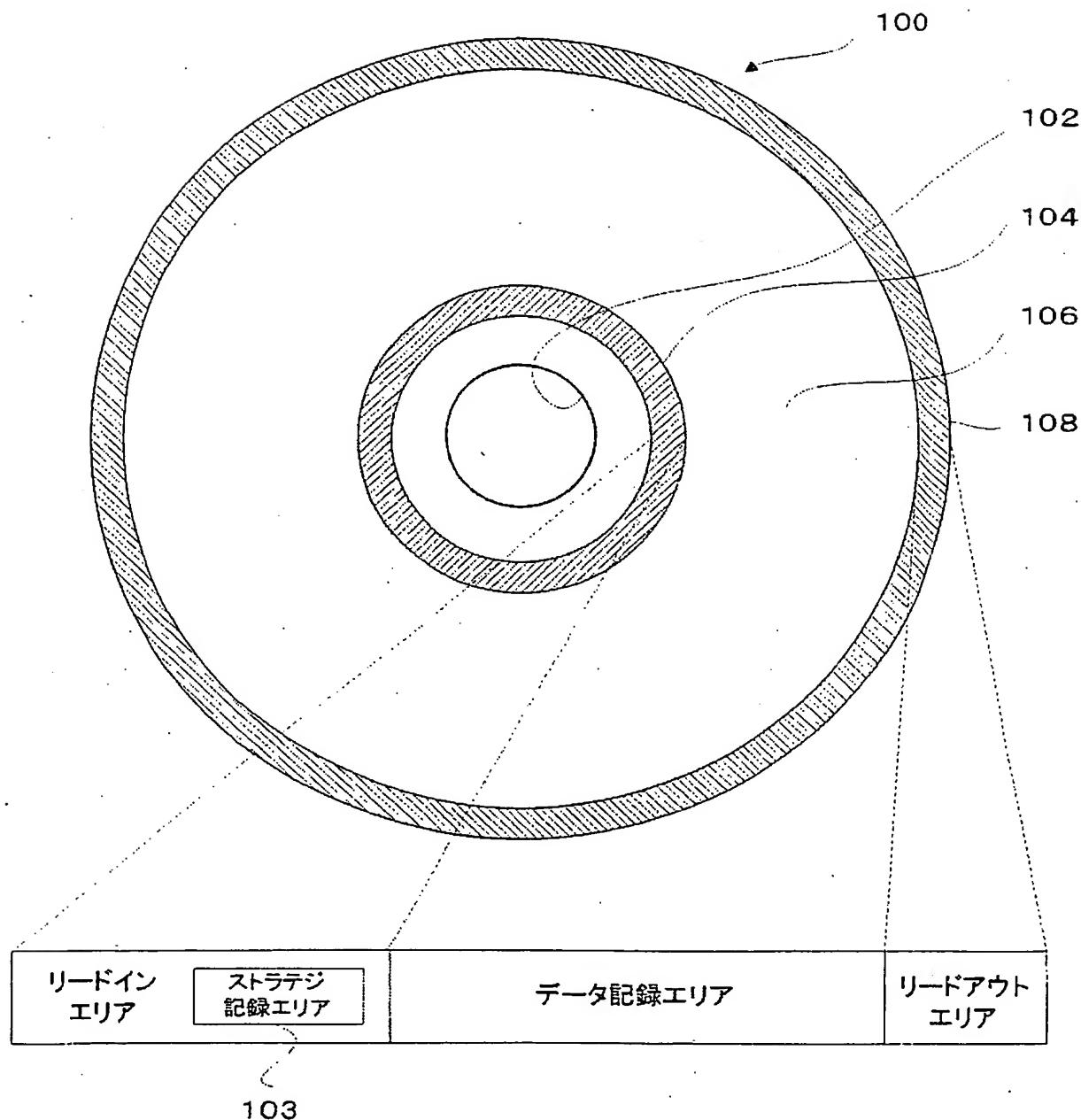


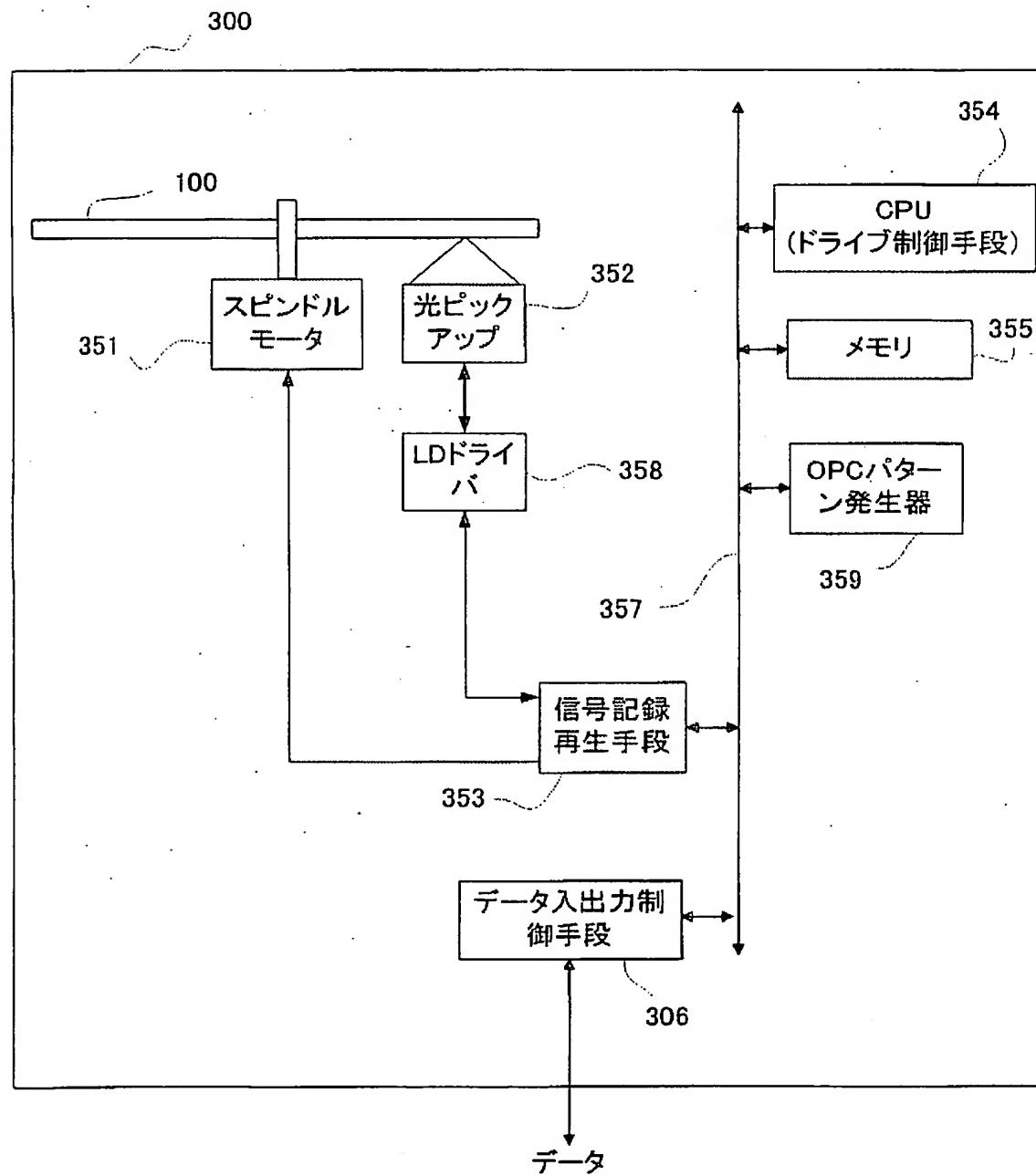
[図1]

図 1



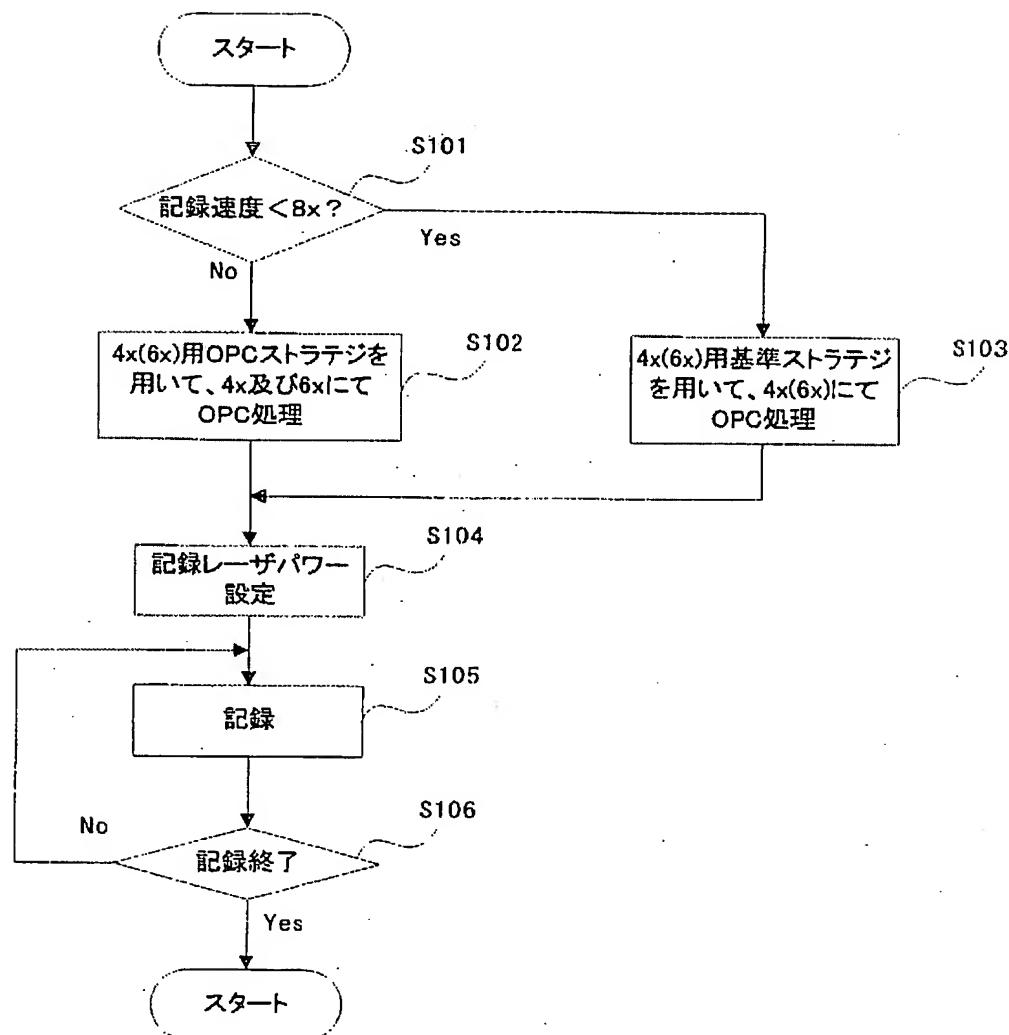
[図2]

図2

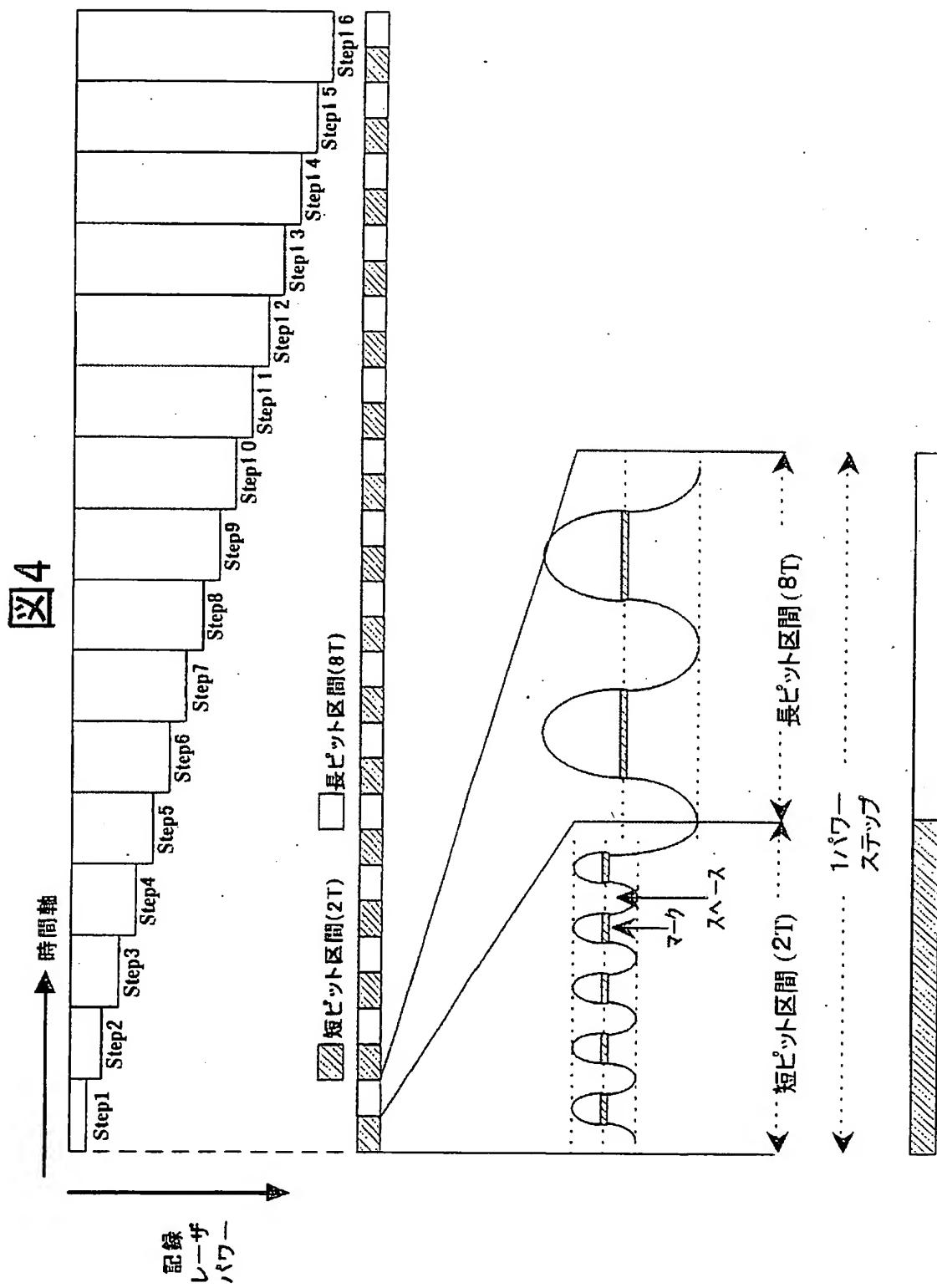


〔図3〕

図3



〔図4〕



[図5]

図5(a)

4x用記録パルス

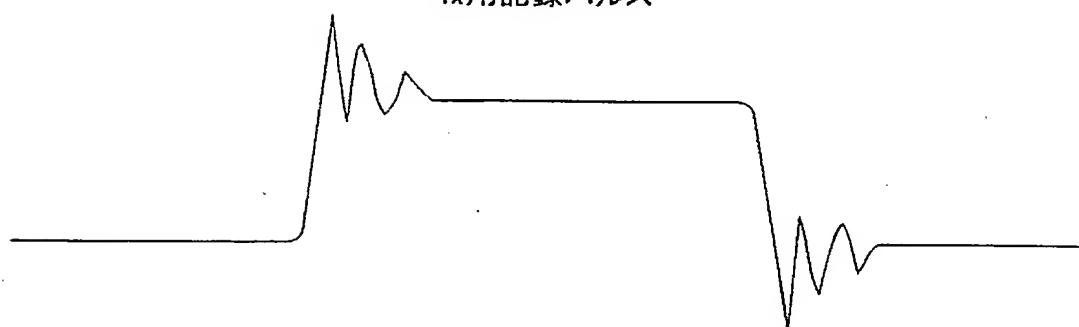


図5(b)

8x用記録パルス

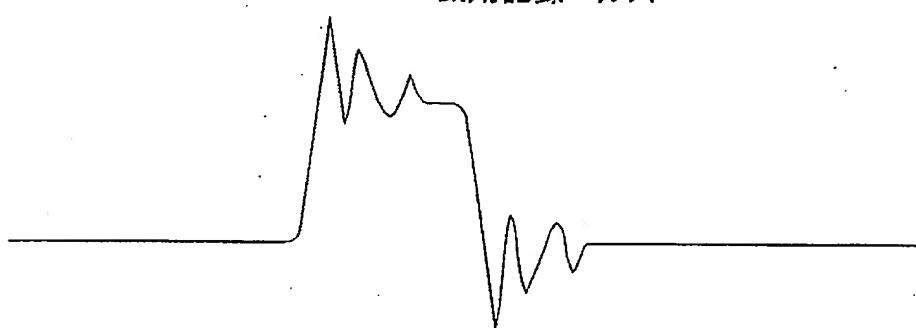
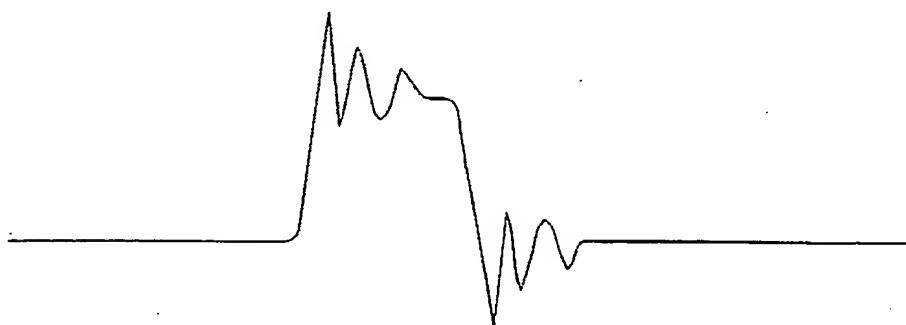
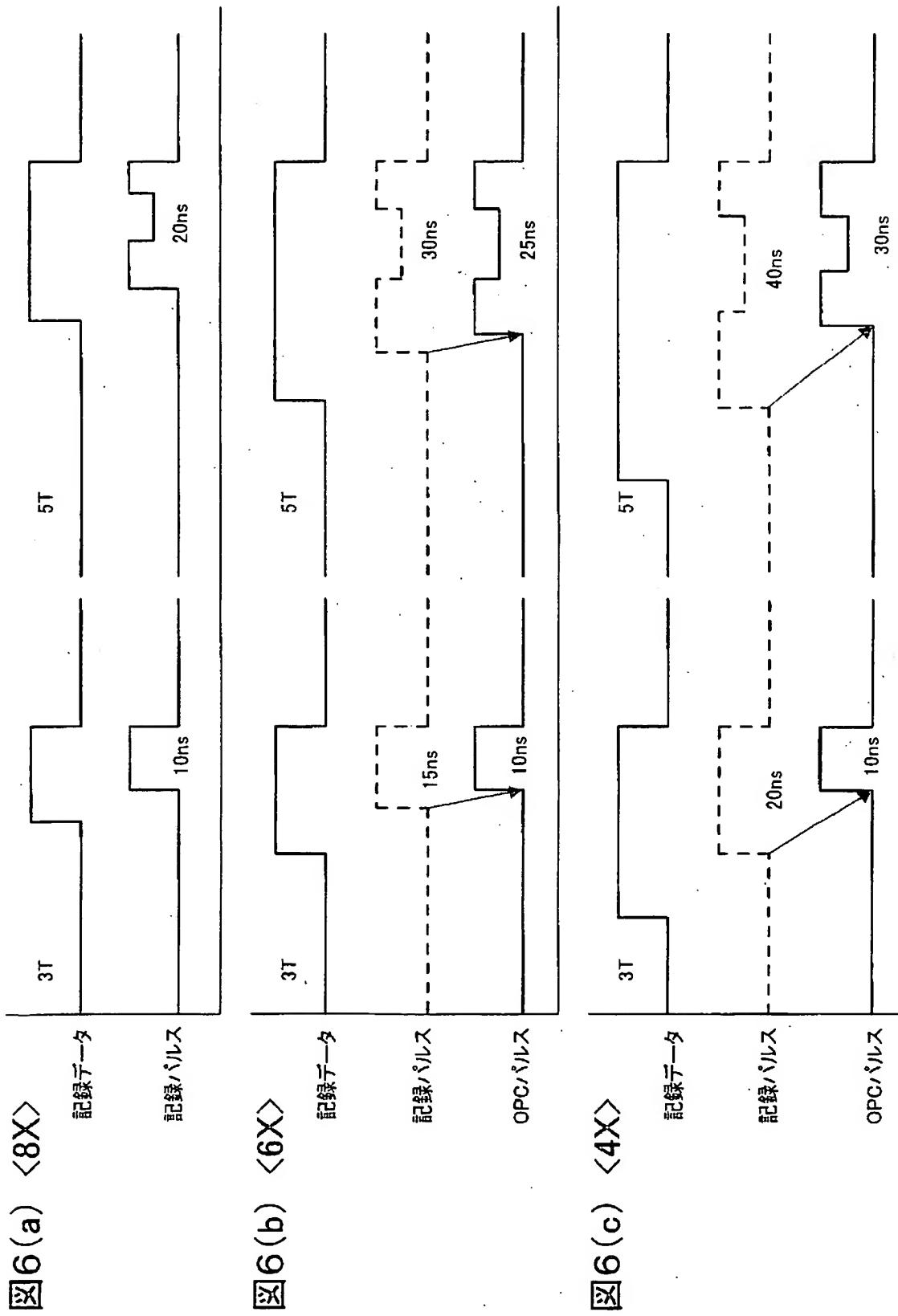


図5(c)

4x用OPCパルス



[図6]



[図7]

図7(a)

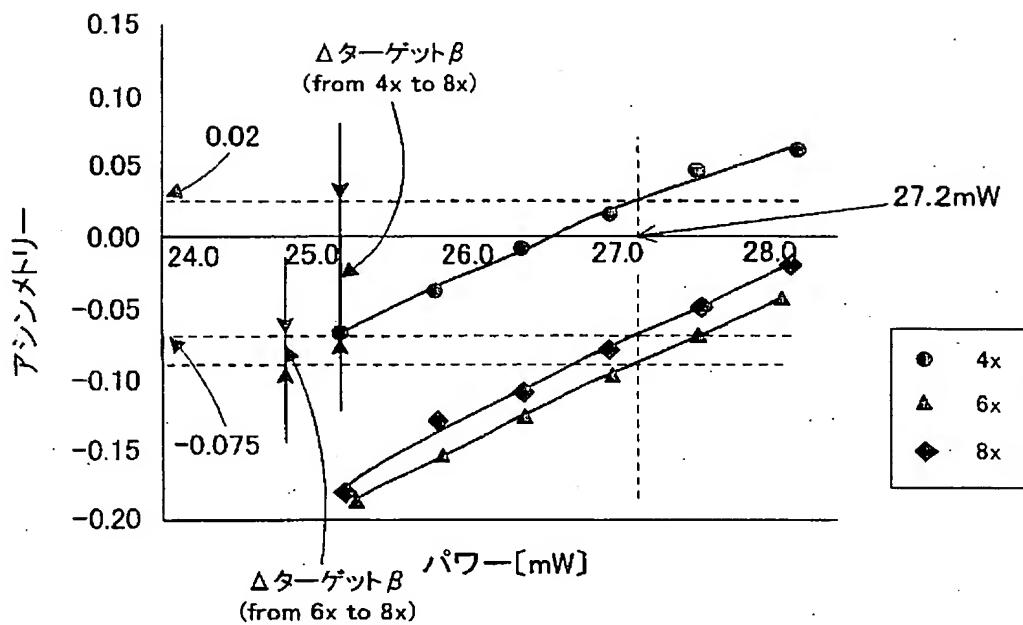
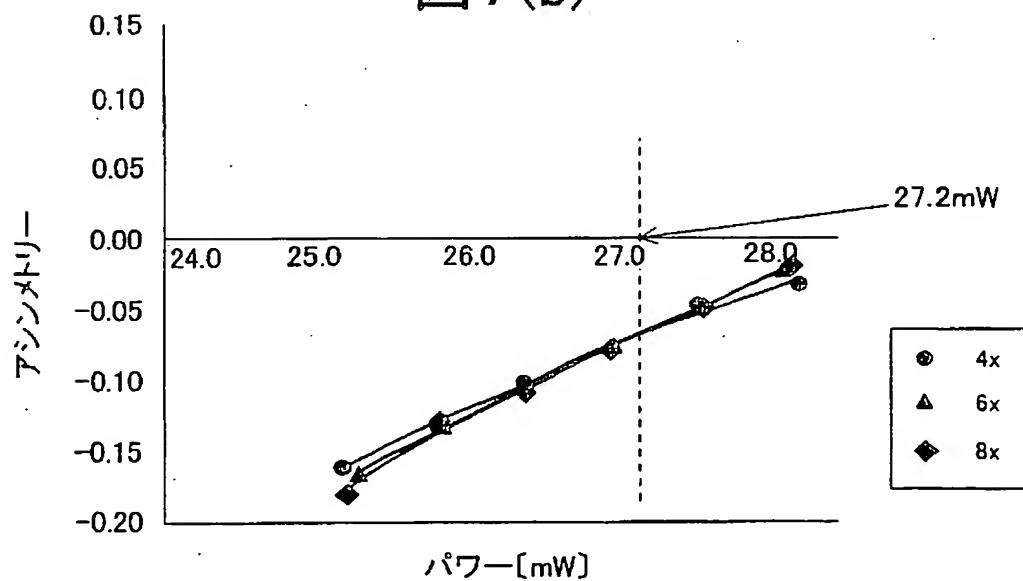


図7(b)



[図8]

図8(a)

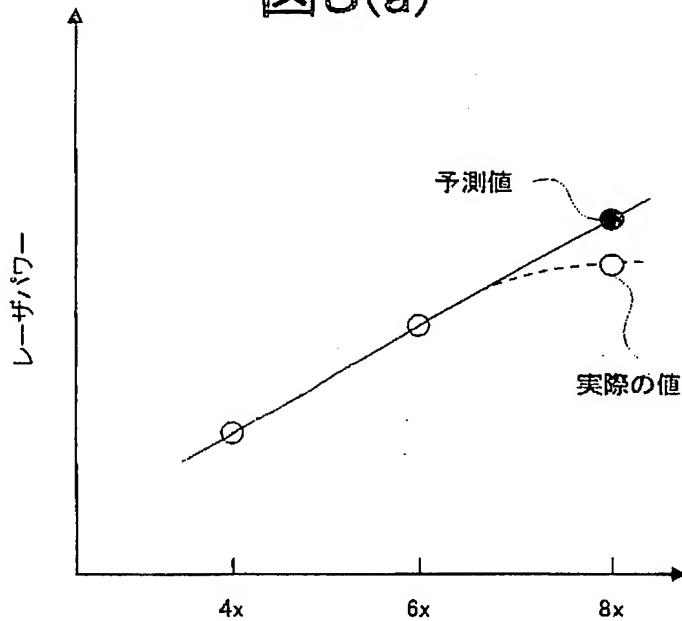


図8(b)

